

REGISTRATION FORM
TWO DAYS NATIONAL WORKSHOP ON
"Hands-on Training in
Analog and Digital VLSI design
using MENTOR GRAPHICS EDA,
ALTERA & TINA-TI"

Name:

Designation:

Organization:

Address for Correspondence:

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Phone :

Email :

Nature of Participants : (Please Tick)

Industry Delegates Academic Staff

Research Scholar Student

Amount Paid:

D.D. Details :

D.D.No. :

Bank Name :

Date :

Whether Accommodation required? Yes / No

Please fill the registration form and mail it to the Coordinator along with the appropriate demand draft drawn in favour of "The Principal, SVS College of Engineering", payable at Coimbatore.

Date : _____ Signature _____

ABOUT THE COLLEGE

The SVS College of Engineering was established in the year 2009 under Srimathi Velammal Subbiah Educational Trust, Coimbatore and it is promoted by Thiru.S.Chandrasekaran, an eminent industrialist to render quality and holistic education, research and training facilities to the younger generation. The main motto of this institution is to impart world class education to the students hailing from rural areas through innovative application of technology and equipping them to develop all round personality by inculcating values of honesty, sincerity and dedicated service to the society. The Trust is supported by a strong team of experts from various fields with Excellent infrastructure and Career development opportunities.

ABOUT THE DEPARTMENT

Electronics and Communication Engineering Department was started in the year 2009 and offers a 4 year B.E. Degree course & 2 year M.E. [VLSI Design] course. The primary objective of the department is to create quality professionals by implementing an interactive and effective teaching methodology with experienced faculty members. The department is well equipped with excellent laboratory infrastructure and modern equipments with worth of Rs. 50 lakhs. The department has state of the art facilities to explore the technological challenges in various fields such as Wireless & Optical networks, VLSI, Signal Processing, Microwave and antennas, Image processing, Embedded systems and many more. To improve the capability and leadership skills of students, various programmes are organized through clubs such as ECE association club, Social service Club-Anbukkarangal and ISTE Students Chapter.

RESOURCE PERSONS

Dr. RAJA RAM, Professor/ECE,
 Thiagarajar College of Engineering,
 Madurai.

Dr. KALPANA, Professor/ECE,
 PSG College of Technology,
 Coimbatore.

Er. SAKTHI MANIKANDAN, Director,
 ADVTECH VLSI Design centre,
 Coimbatore.

Er. C. PRAKASHAM, Technical Lead Trainer,
 ADVTECH VLSI Design centre,
 Coimbatore.

For further details please contact

Prof. S. SENTHIL KUMAR, HoD/ECE
SVS College of Engineering,
Arasampalayam, Coimbatore - 642109.
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SVS TWO DAYS NATIONAL WORKSHOP ON

"Hands-on Training in
Analog and Digital VLSI design
using MENTOR GRAPHICS EDA,
ALTERA & TINA-TI"
18th & 19th October 2013

Organised by
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING



SVS COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi, Affiliated to Anna University, Chennai, ISO 9001:2008 Certified Institution)
 Arasampalayam, Coimbatore-642109.

Phone: 0422-2619301-05, www.svscolleges.org

ABOUT THE WORKSHOP

Two days national workshop on "Hands on training in Analog and Digital VLSI design Using Mentor Graphics EDA, Altera & TINA-TI" is designed to give a flavor of industry oriented VLSI training on selected concepts, by making the students do hands on labs with specific tools available in the college. The key objective is to provide an informal forum for vigorous creative discussion and debate on this arena. The workshop will bring experts together from the design automation and parallel programming community to discuss the timely issue of parallelization of EDA tools. Additionally, the workshop is intended to create awareness of present scenario. The workshop will put forward the opportunities to flourish the knowledge and to pursue research in this area. The workshop will provide a fruitful insight of the current status of VLSI design techniques and give the generic measure of fundamental issues in VLSI design. Participants will be able to access expert opinion on VLSI design techniques and will be encouraged to share their own assessment of practical utility.

The course content includes:

- ASIC & FPGA Design & Verification Flow
- SOC, Bi-CMOS Technology
- Submicron issues and MOSFET submicron models
- Cell Library design, Cell layout & Chip floor planning
- Hands-on training in TINA -TI, EDA tool and FPGA Device.

Registration Details:

Please note that the delegates should register and pay the requisite fee. The registration fee includes seminar proceedings and lunch during the workshop.

A maximum of 50 registrations will be accepted on first come first serve basis.

Participants	Registration fee
Research Scholars / PG students	Rs. 300/-
Faculty members	Rs. 400/-
Delegates from industry	

ORGANIZING COMMITTEE

- Chief Patron** Thiru.S.Chandrasekaran, Chairman, SVS Educational Institutions
- Patron** Mr.B.Raghavendran, B.E., M.B.A.(U.K.)
 Vice chairman, SVS Educational Institutions
- Chairman** Dr.T.Kannan, Principal
- Convener** Mr.S.Senthil Kumar, HoD
 Department of ECE
- Co-ordinators** Mr.K.Kalirajan, Assistant Professor/ECE
 Mrs.M.Archana, Assistant Professor/ECE

Dates to Remember:

- Last date for Receipt of application : 10.10.2013
- Date of Confirmation by Email : 12.10.2013
- Date of Workshop : 18th & 19th ,October 2013